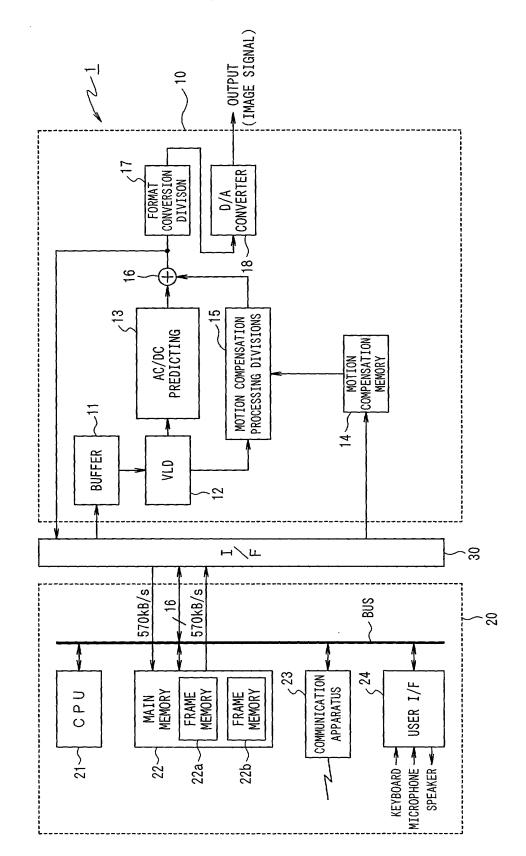
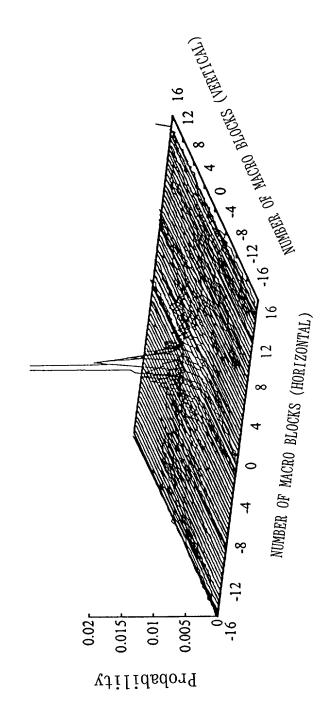
FIG. 1



F I G. 2



F I G. 3

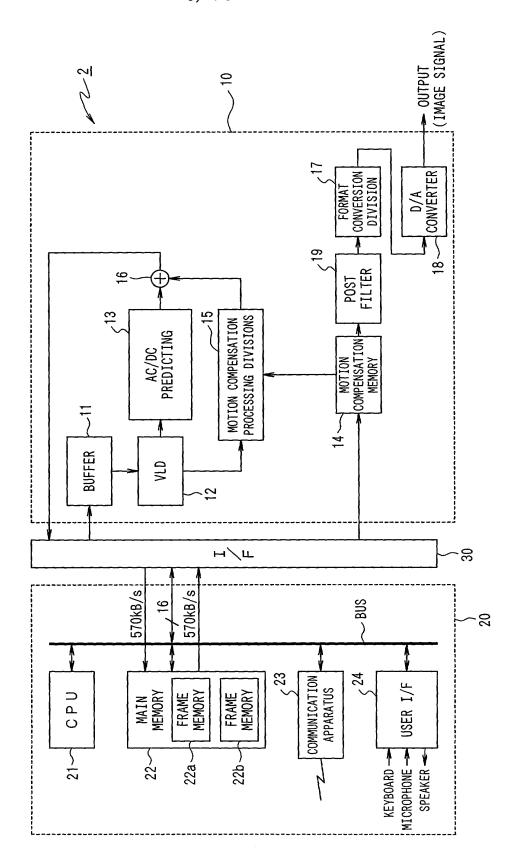
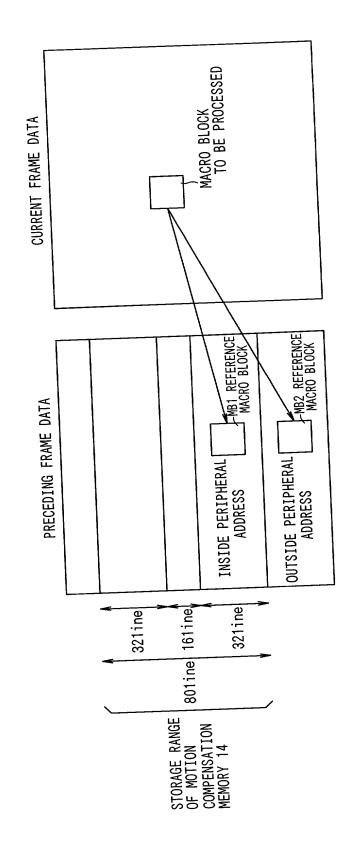


FIG.

(IMAGE SIGNAL) က 9 D/A CONVERTER FORMAT CONVERSION DIVISION 18 \ POST FILTER PROCESSING DIVISIONS MOTION COMPENSATION MEMORY AREA ADMINISTRATION DIVISION AC/DC PREDICTING MOTION COMPENSATION = 14 BUFFER ۸۲D  $H \setminus H$ 9 570kB/s 570kB/s > BUS 16 29, COMMUNICATION USER I/F FRAME MEMORY APPARATUS MAIN MEMORY FRAME MEMORY  $\overset{\mathsf{P}}{\cup}$  $\circ$ 22~ 22a~ 22b~ 21 MICROPHONE -SPEAKER 🖛 KEYBOARD 🚤

F I G. 5

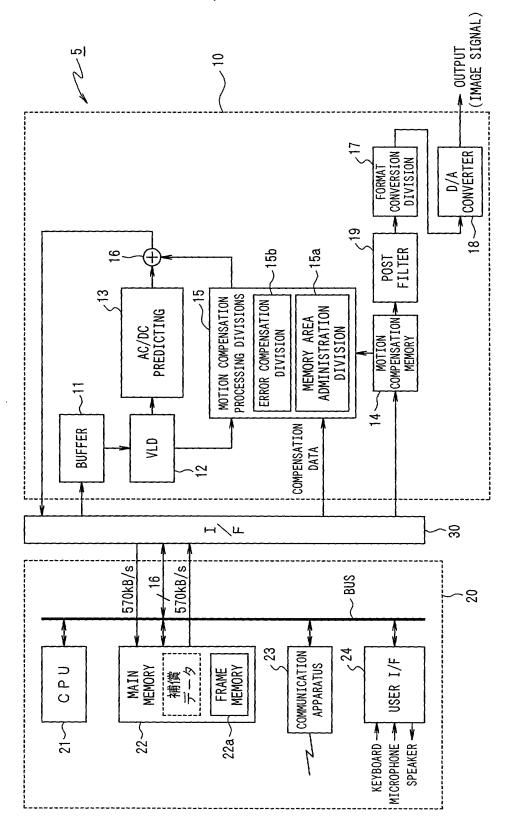


OUTPUT (IMAGE SIGNAL) FORMAT CONVERSION DIVISION CONVERTER ) % POST FILTER ~15a 16 5 MOTION COMPENSATION PROCESSING DIVISIONS ERROR COMPENSATION DIVISION MEMORY AREA ADMINISTRATION AC/DC PREDICTING MOTION COMPENSATION --MEMORY DIVISION 77 DOWNWARD AREA DATA BUFFER 7LD 2  $H \setminus L$ 570kB/s 570kB/s ∠ BUS 16 20 COMMUNICATION APPARATUS 733 USER I/F FRAME MEMORY MAIN MEMORY  $_{\mathsf{D}}^{\mathsf{P}}$ O 22~ 22a — 21~ KEYBOARD 🚽

6/10

FIG.  $\theta$ 

F I G. 7



F I G. 8
16 PIXELS

F I G. 9
16 PIXELS

DC COMPONENT

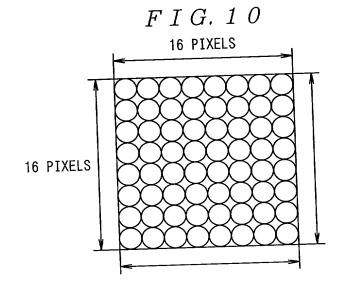


FIG. 11

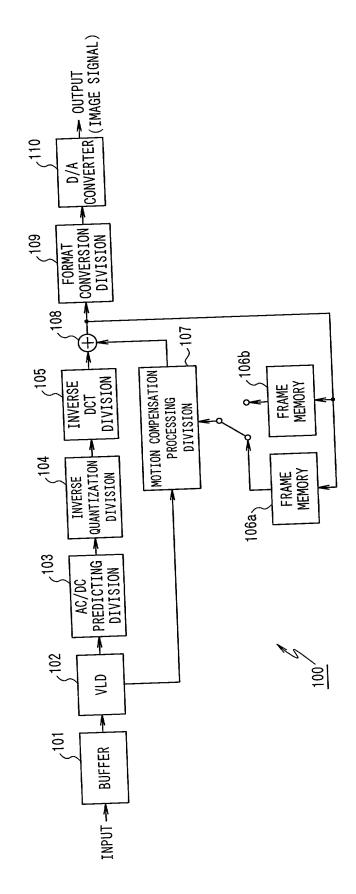


FIG. 12

